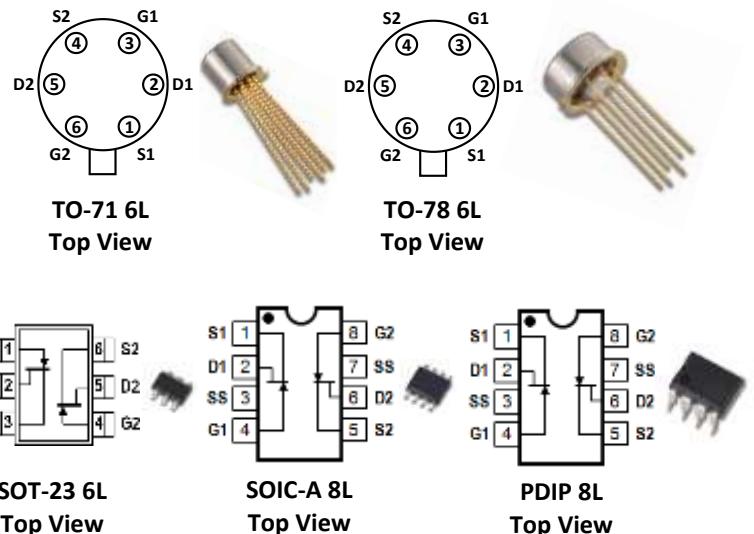


LOW INPUT CAPACITANCE MONOLITHIC DUAL N-CHANNEL JFET

FEATURES		
Ultra-Low Noise	$e_n=2.5\text{nV}/\text{Hz}$ @ 1kHz TYP.	
Low Leakage	$I_G=15\text{pA}$ TYPs.	
Low Drift	$ V_{GS1-2} /T=5\mu\text{V}/^\circ\text{C}$ max.	
Ultra-Low Offset Voltage	$ V_{GS1-2} =1\text{mV}$ max.	
ABSOLUTE MAXIMUM RATINGS ¹		
@ 25°C (unless otherwise noted)		
Maximum Temperatures		
Storage Temperature	-55° to +150°C	
Operating Junction Temperature	-55° to +150°C	
Maximum Voltage and Current for Each Transistor ¹		
-V _{GSS}	Gate Voltage to Drain or Source	60V
I _{G(f)}	Gate Forward Current	50mA
Maximum Power Dissipation ²		
Device Dissipation ² @ Free Air - Total	400mW T _A =+25°C	



* For equivalent single version, see LSK189

Features

- Low Noise: $e_n = 2.5\text{nV}/\sqrt{\text{Hz}}$ (typ), $f = 1\text{kHz}$, NBW = 1Hz
- Very Low Common Source Input Capacitance of $C_{iss} = 3\text{pF}$ – typ and 8pF max
- High Slew Rate
- Low Offset/Drift Voltage
- Low Gate Leakage I_{GSS} and I_G
- High CMRR 102 dB

Benefits

- Tight Differential Voltage Match vs. Current
- Improved Op Amp Speed Settling Time Accuracy
- Minimum Input Error Trimming Error Voltage
- Lower Intermodulation Distortion

Applications

- Wideband Differential Amplifiers
- High Speed Temperature Compensated Single Ended Input Amplifier Amps
- High Speed Comparators
- Impedance Converters
- Sonobouys and Hydrophones
- Acoustic Sensors

Description

The LS844 Series is the industry's lowest input capacitance and low-noise monolithic dual N-Channel JFET. Low input capacitance substantially reduces intermodulation distortion. In addition, these dual JFETs feature tight offset voltage and low drift over temperature range, and are targeted for use in a wide range of precision instrumentation and sensor applications.

The LS844 Series is available in surface mount plastic SOIC 8L, PDIP 8L and SOT-23 6L packages. Additionally, it is offered in thru-hole metal cans; the TO-71 6L and TO-78 6L package.

For an equivalent single N-Channel version refer to the LSK189 datasheet. LS844 Series TO-71 6L and SOIC 8L are fit, form and pin compatible to the same LSK389 product.

The LS844 Series provides an increase in capabilities for a wide range of low-noise applications.

The most significant aspect of the LS844 Series is how it combines a noise level comparable with the LSK389 while having much lower gate-to-drain capacitance, 4pF versus the 25pF. The slightly higher noise of the LS844 Series, versus the LSK389, is not significant in most instances, while the much lower capacitance enables designers to produce simpler, more elegant circuit designs with fewer devices that cost less in production.

Like the Linear Systems LSK389, the LS844 Series features a unique design construction of interleaving both JFETs on the same piece of silicon to provide excellent matching and thermal tracking, as well a low-noise profile having nearly zero popcorn noise.

Electrical Characteristics @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTIC	LS843	LS844	LS845	UNITS	CONDITIONS
V _{GS1-2} - T max.	Drift vs. Temperature	5	10	25	µV/°C	V _{DG} = 10V I _D = 500µA T _A = -55°C to +125°C
V _{GS1-2} max.	Offset Voltage	1	5	15	mV	V _{GS} = 10V I _D = 500µA

SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNITS	CONDITIONS
BV _{GSS}	Breakdown Voltage	-60	--	--	V	V _{DS} = 0 I _D = -1nA
BV _{GGO}	Gate-to-Gate Breakdown	±60	--	--	V	I _{GGO} = ±1µA I _D = 0 I _S = 0
G _{fss}	TRANSCONDUCTANCE Full Conduction	1500	--	--	µS	V _{DS} = 15V V _{GS} = 0 f = 1kHz
G _{fs}	Typical Conduction	1000	1500	--	µS	V _{DS} = 15V I _D = 500µA
G _{fs1-2} /G _{fs1}	Mismatch	--	0.6	3	%	
I _{DSS}	DRAIN CURRENT Full Conduction	1.5	5	15	mA	V _{DS} = 15V V _{GS} = 0
I _{DSS1-2} /I _{DSS}	Mismatch at Full Conduction	--	1	5	%	
V _{GS(off)}	GATE VOLTAGE Pinchoff Voltage	-1	--	-3.5	V	V _{DS} = 15V I _D = 1nA
V _{GS}	Operating Range	-0.5	--	-3.5	V	V _{DS} = 15V I _D = 500µA
-I _G	GATE CURRENT Operating	--	15	50	pA	V _{DG} = 15V I _D = 500µA
-I _G	High Temperature	--	--	50	nA	V _{DG} = 15V I _D = 500µA T _A =+125°C
-I _G	Reduced VDG	--	5	30	pA	V _{DG} = 3V I _D = 500µA
-I _{GSS}	At Full Conduction	--	--	100	pA	V _{GS} = 15V V _{GS} = 0
G _{oss}	OUTPUT CONDUCTANCE Full Conduction	--	--	40	µS	V _{DS} = 15V V _{GS} = 0
G _{os} G _{os1-2}	Operating Differential	--	2.0	2.7	µS	V _{DS} = 15V I _D = 200µA
		--	0.02	0.2	µS	

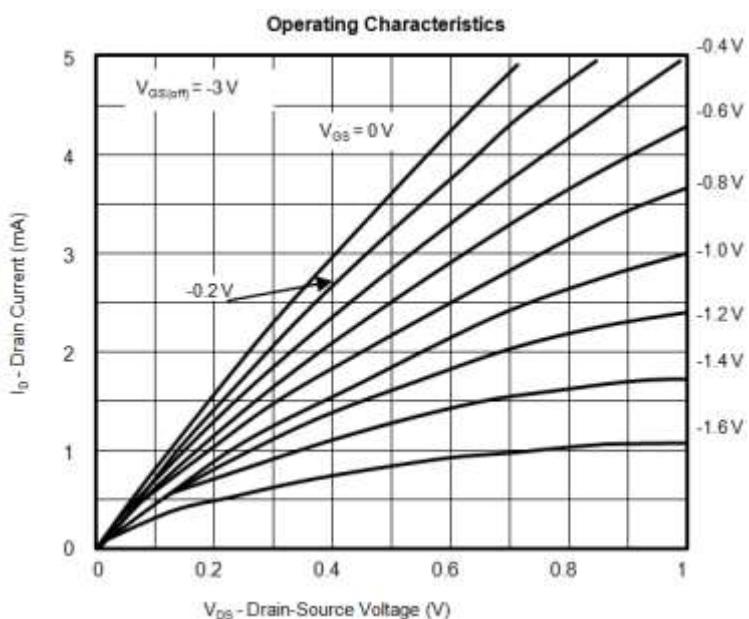
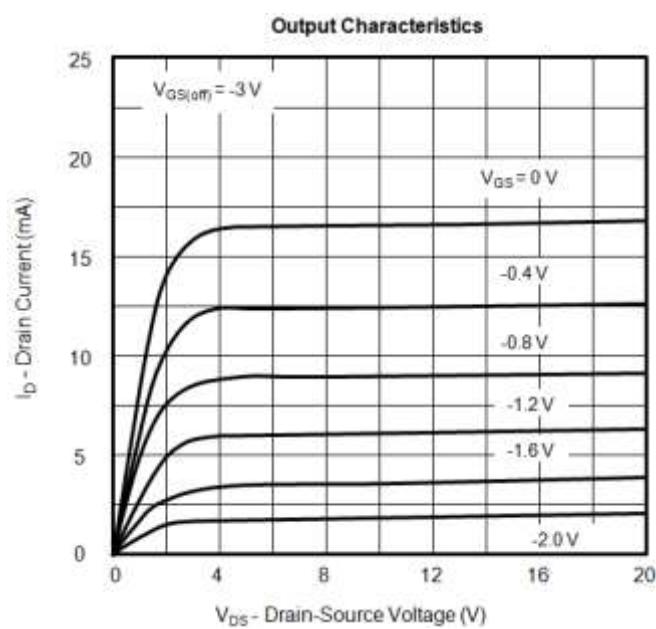
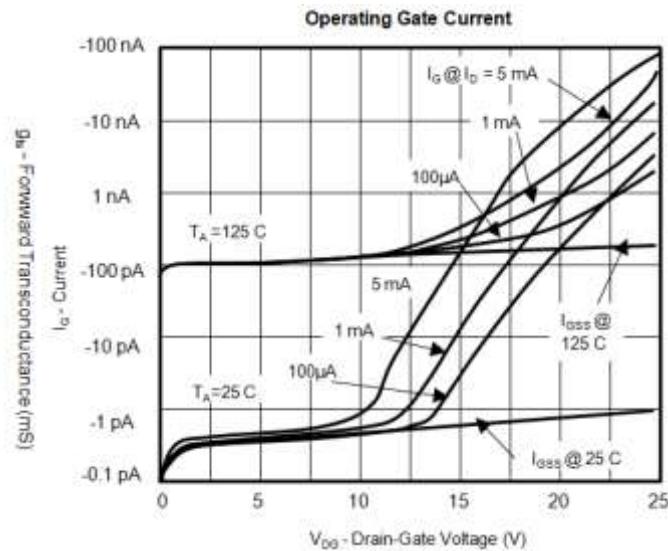
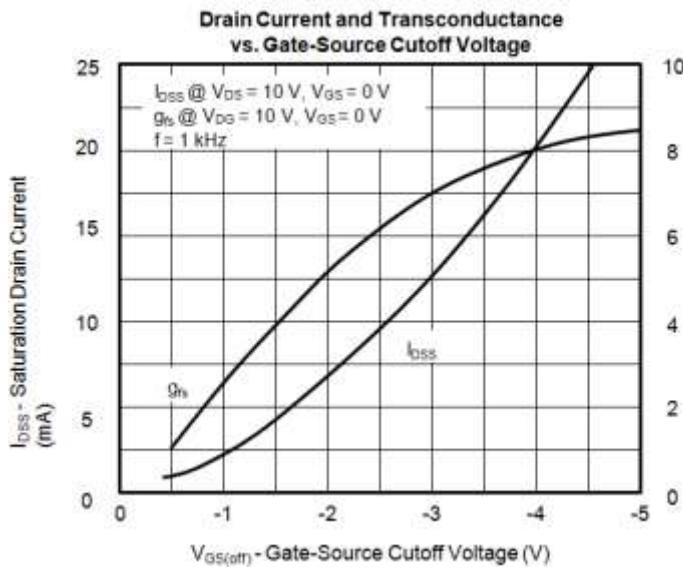
SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNITS	CONDITIONS
	<u>COMMON MODE REJECTION</u>					
CMRR	$-20 \log \Delta V_{GS1-2} / \Delta V_{DS} $	90	100	--	dB	$V_{DS} = 10$ to $20V$ $I_D = 500\mu A$
CMRR		--	85	--	dB	$V_{DS} = 5$ to $10V$ $I_D = 500\mu A$
e_n	Voltage	--	--	3.0	nV/Hz	$V_{DS} = 15V$ $I_D = 2.0mA$ $f = 1kHz$ $NBW = 1Hz$
e_n	Voltage	--	--	7.0	nV/Hz	$V_{DS} = 15V$ $I_D = 2.0mA$ $f = 10Hz$ $NBW = 1Hz$
C_{ISS}	<u>CAPACITANCE</u> Input				pF	$V_{DS} = 15V$ $I_D = 500\mu A$ $f = 1mHz$
C_{RSS}	Reverse Transfer	--	--	3	pF	
C_{DD}	Drain-to-Drain	--	0.5	--	pF	$V_{DD} = 15V$ $I_D = 500\mu A$ $f = 1mHz$

Notes:

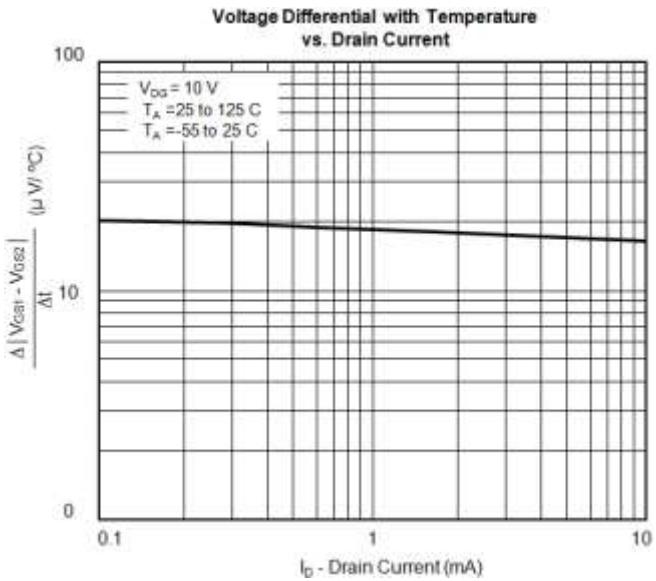
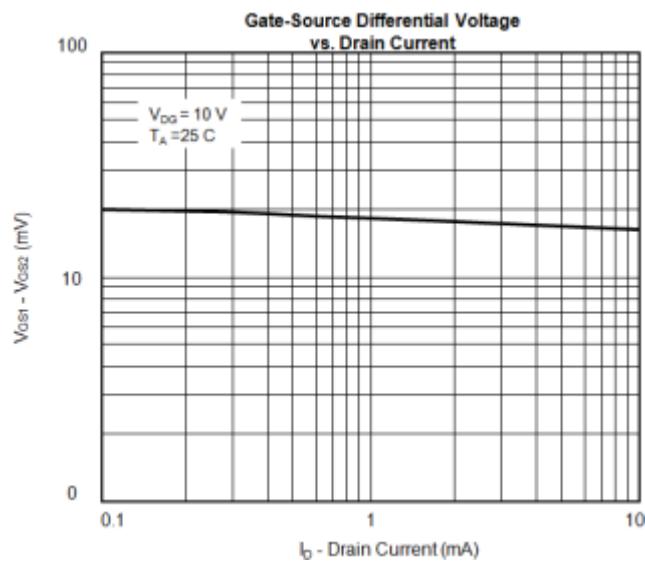
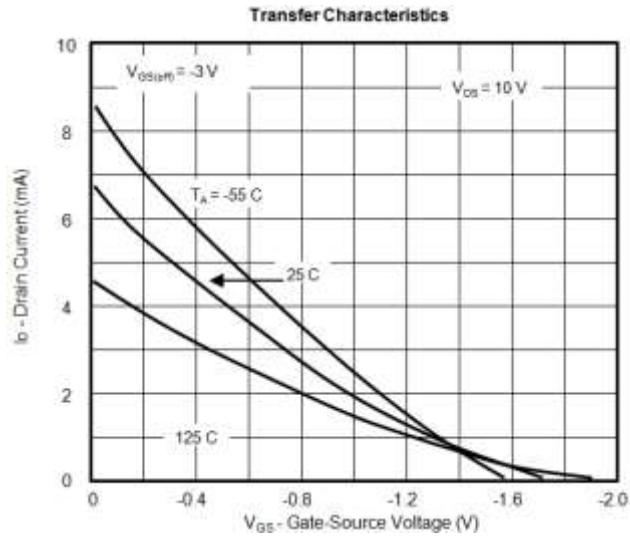
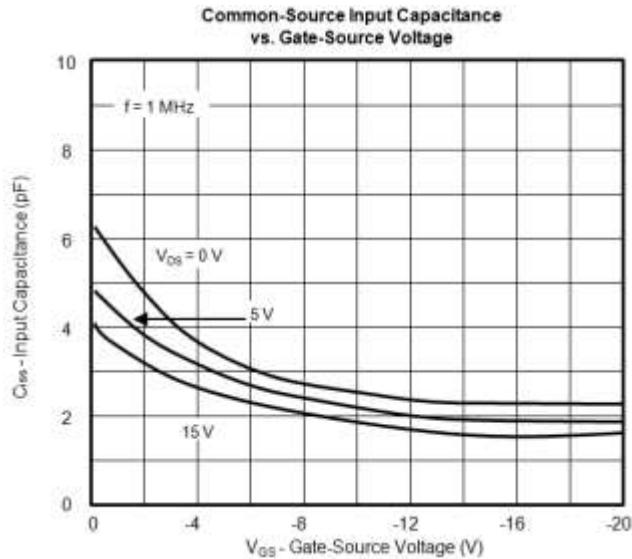
1. Absolute maximum ratings are limiting values above which serviceability may be impaired.
2. Pulse width $\leq 2ms$.
3. All MIN/TYP/MAX Limits are absolute values. Negative signs indicate electrical polarity only.
4. Derate $2.4 \text{ mW}/^{\circ}\text{C}$ above 25°C .
5. Derate $4 \text{ mW}/^{\circ}\text{C}$ above 25°C .

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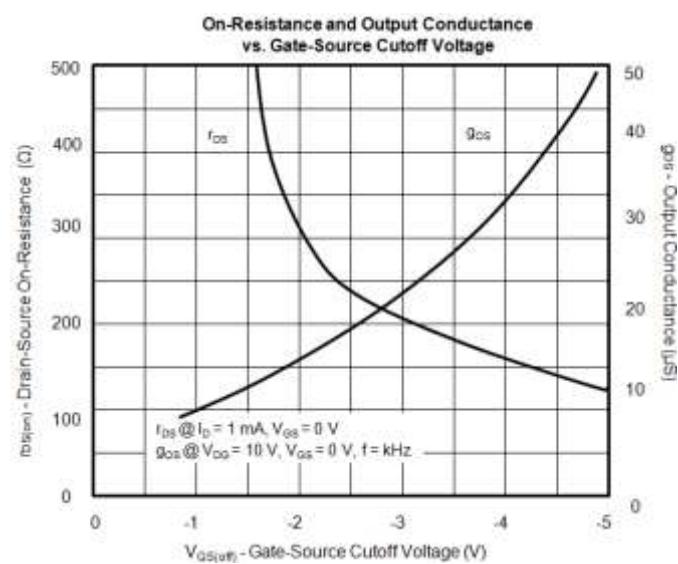
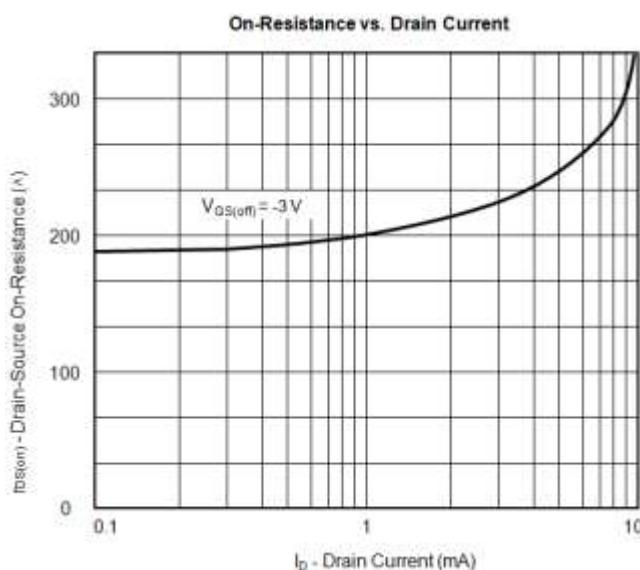
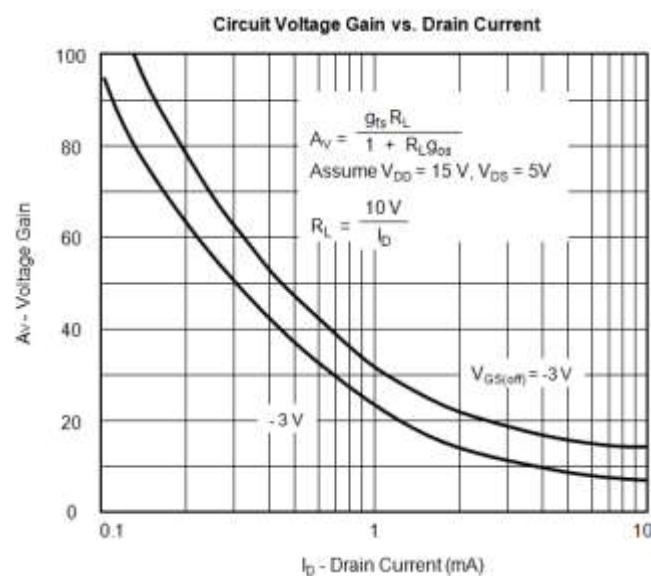
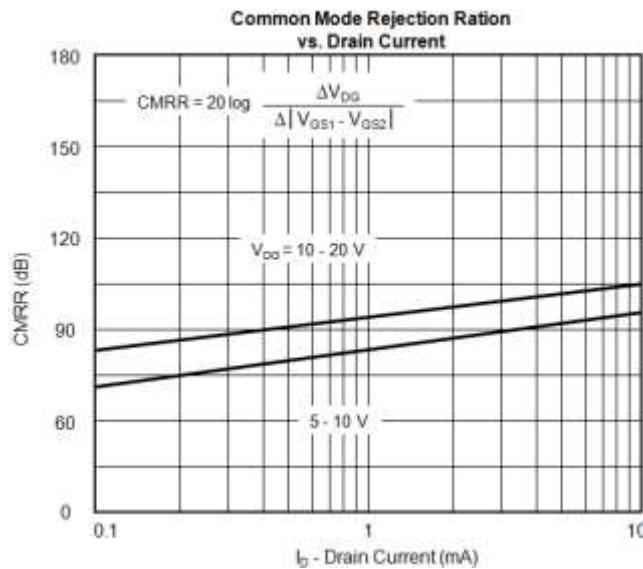
Typical Characteristics



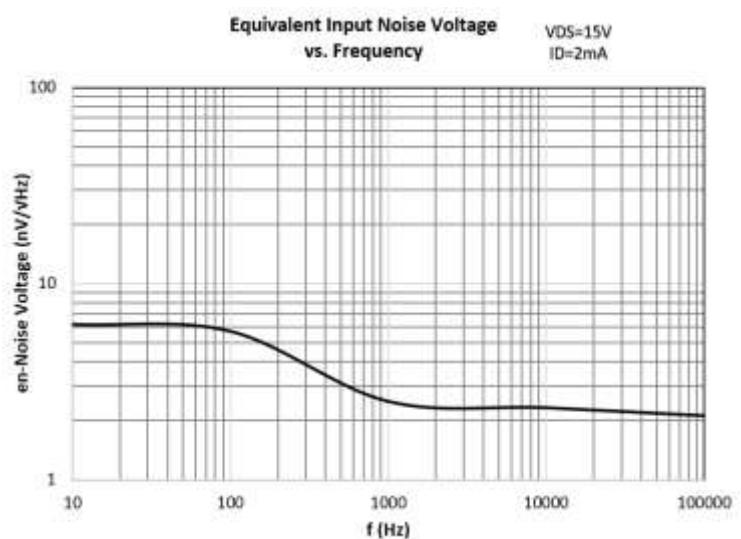
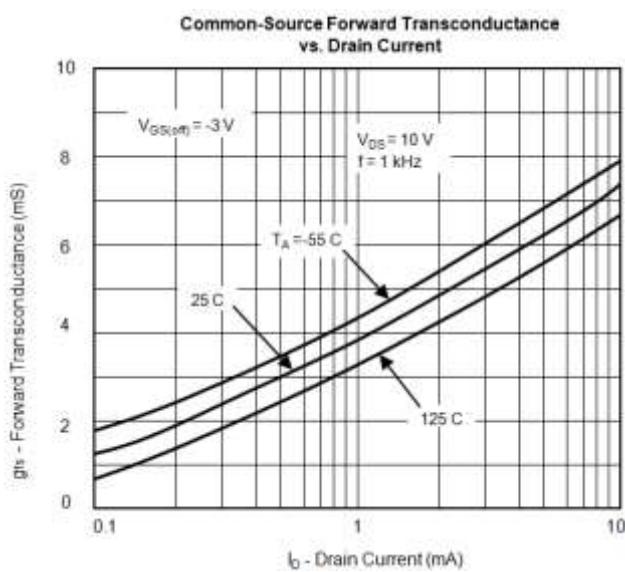
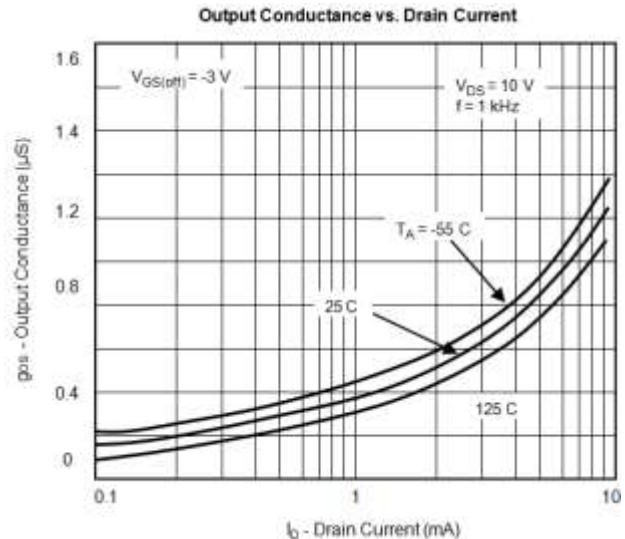
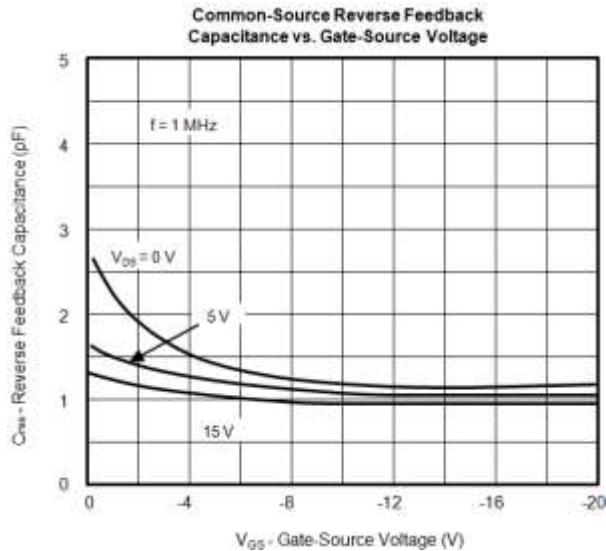
Typical Characteristics Continued



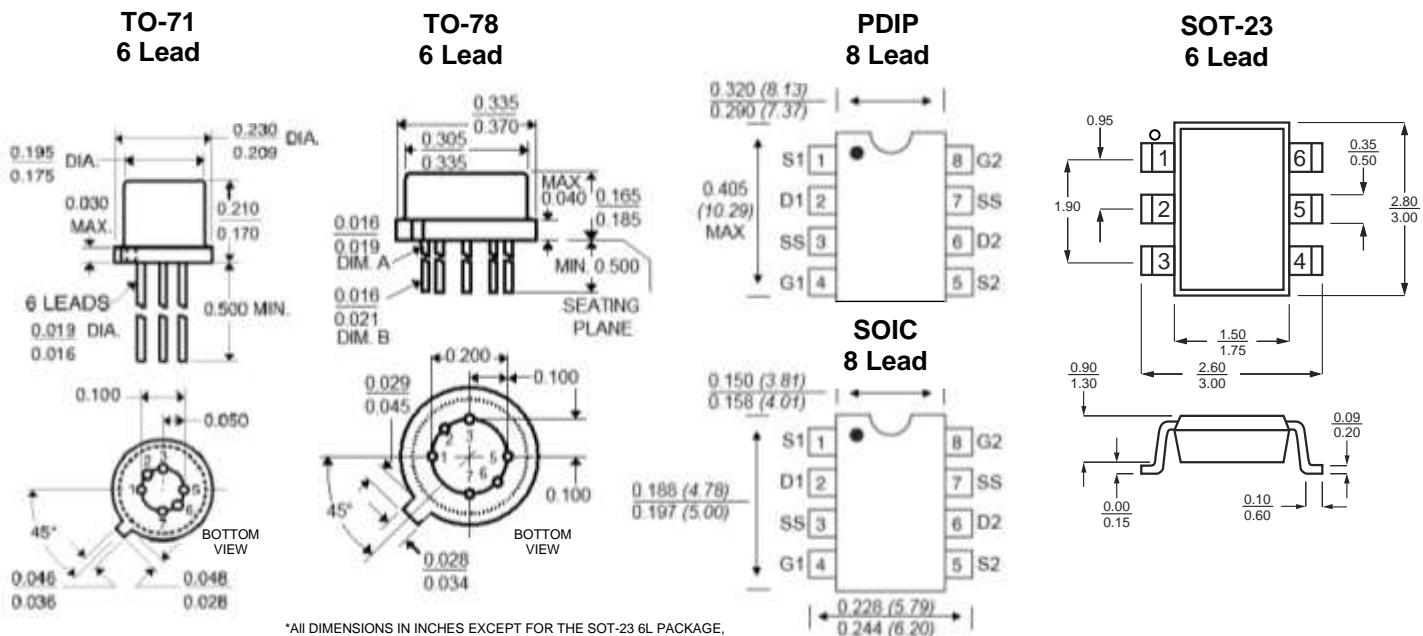
Typical Characteristics Continued



Typical Characteristics Continued



Package Dimensions:



Ordering Information:

Standard Part Call-Out		
LS843 TO-71 6L RoHS	LS844 TO-71 6L RoHS	LS845 TO-71 6L RoHS
LS843 TO-78 6L RoHS	LS844 TO-78 6L RoHS	LS845 TO-78 6L RoHS
LS843 SOT-23 6L RoHS	LS844 SOT-23 6L RoHS	LS845 SOT-23 6L RoHS
LS843 SOIC 8L RoHS	LS844 SOIC 8L RoHS	LS845 SOIC 8L RoHS
LS843 PDIP 8L RoHS	LS844 PDIP 8L RoHS	LS845 PDIP 8L RoHS
Custom Part Call-Out (Custom Parts Include SEL + 4 Digit Numeric Code)		
LS843 TO-71 6L RoHS SELXXXX	LS844 TO-71 6L RoHS SELXXXX	LS845 TO-71 6L RoHS SELXXXX
LS843 TO-78 6L RoHS SELXXXX	LS844 TO-78 6L RoHS SELXXXX	LS845 TO-78 6L RoHS SELXXXX
LS843 SOT-23 6L RoHS SELXXXX	LS844 SOT-23 6L RoHS SELXXXX	LS845 SOT-23 6L RoHS SELXXXX
LS843 SOIC 8L RoHS SELXXXX	LS844 SOIC 8L RoHS SELXXXX	LS845 SOIC 8L RoHS SELXXXX
LS843 PDIP 8L RoHS SELXXXX	LS844 PDIP 8L RoHS SELXXXX	LS845 PDIP 8L RoHS SELXXXX