

## P-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The G3401L uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> -30V</li> <li>● <math>I_D</math> (at <math>V_{GS} = -10V</math>) -4.4A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -10V</math>) &lt; 55mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -4.5V</math>) &lt; 65mΩ</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -2.5V</math>) &lt; 90mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	<p>Schematic diagram</p> <p>SOT-23-3L</p>
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<b>Ordering Information</b>			
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
G3401L	SOT-23-3L	3401	3000pcs/Reel

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Drain-Source Voltage	$V_{DS}$	-30	V
Continuous Drain Current	$I_D$	-4.4	A
Pulsed Drain Current (note1)	$I_{DM}$	-17.6	A
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Power Dissipation	$P_D$	1.3	W
Single pulse avalanche energy (note2)	$E_{AS}$	14	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

<b>Thermal Resistance</b>			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	96	°C/W

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -30\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	-1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 12\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.6	-0.8	-1.3	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -4\text{A}$	--	42	55	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -4\text{A}$	--	50	65	
		$V_{\text{GS}} = -2.5\text{V}, I_D = -2\text{A}$	--	67	90	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = -5\text{V}, I_D = -4\text{A}$	--	10	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -15\text{V}, f = 1.0\text{MHz}$	--	652	--	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		--	74	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	66	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = -15\text{V}, I_D = -4\text{A}, V_{\text{GS}} = -4.5\text{V}$	--	9.3	--	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		--	2	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	2.9	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -15\text{V}, I_D = -4\text{A}, R_G = 3\Omega$	--	6.8	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	2.9	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	29	--	
Turn-off Fall Time	$t_f$		--	12	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	-4.4	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = -4\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	-1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = -4\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = -100\text{A}/\mu\text{s}$	--	4.5	--	$\text{nC}$
Reverse Recovery Time	$T_{\text{rr}}$		--	9	--	ns

**Notes**

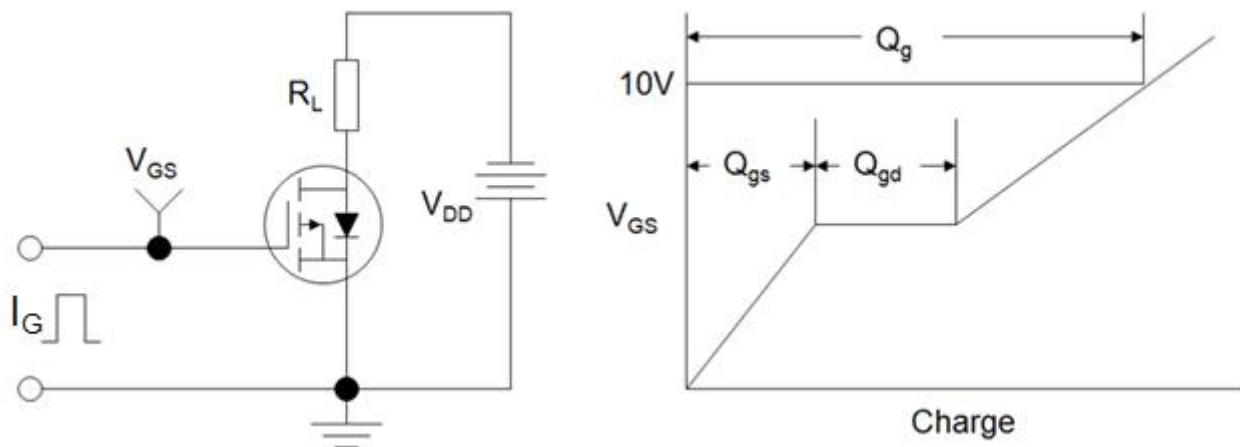
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=-30\text{V}$ ,  $V_{\text{GS}}=-10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$

The table shows the minimum avalanche energy, which is 39mJ when the device is tested until failure

3. Identical low side and high side switch with identical  $R_G$

Gate Charge Test Circuit



Switch Time Test Circuit

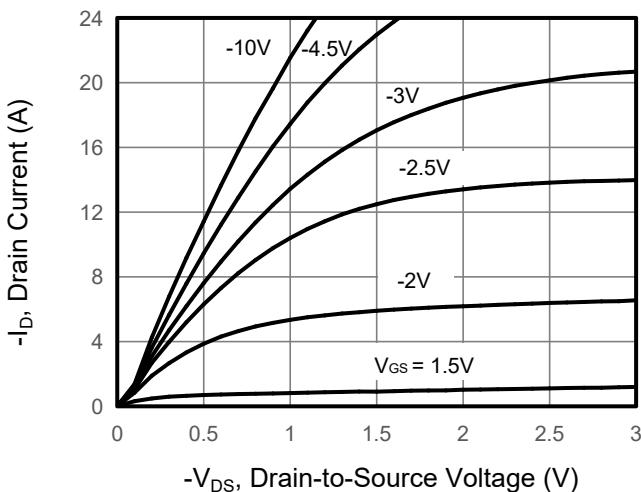


EAS Test Circuit

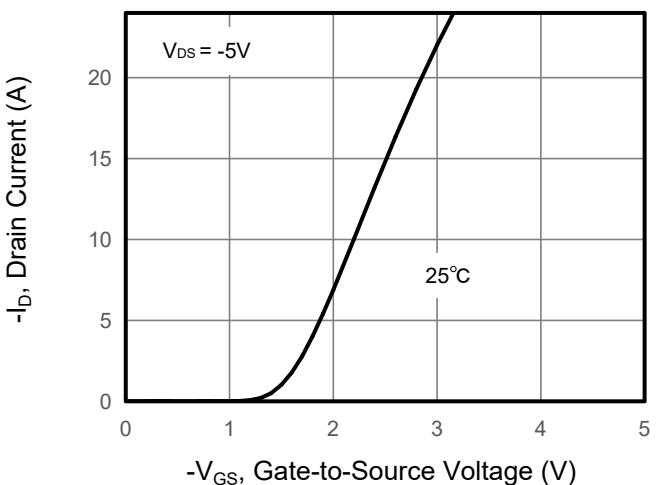


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

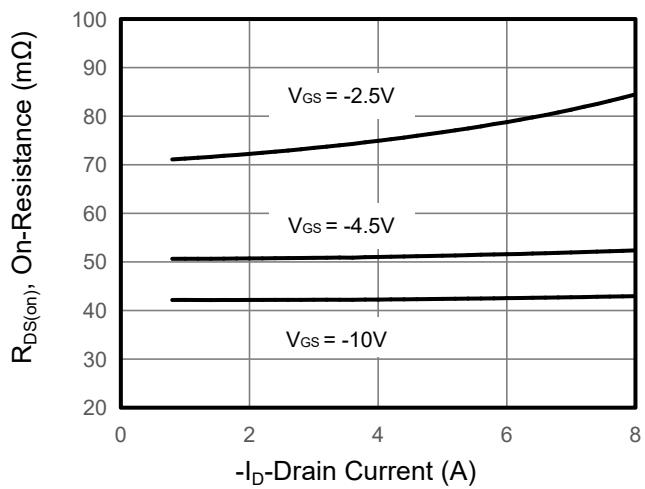
**Figure 1. Output Characteristics**



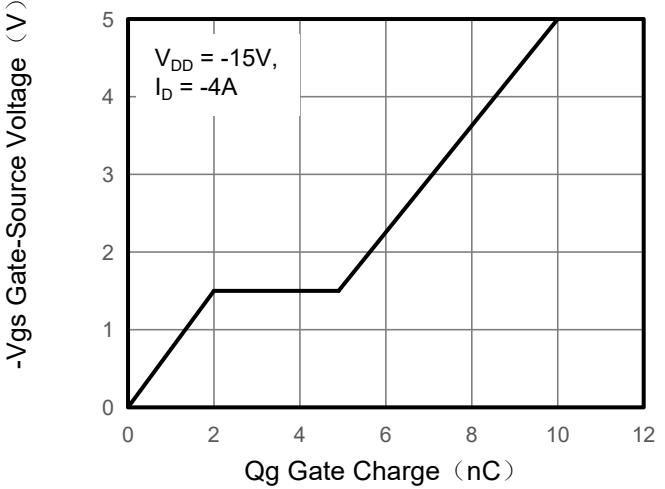
**Figure 2. Transfer Characteristics**



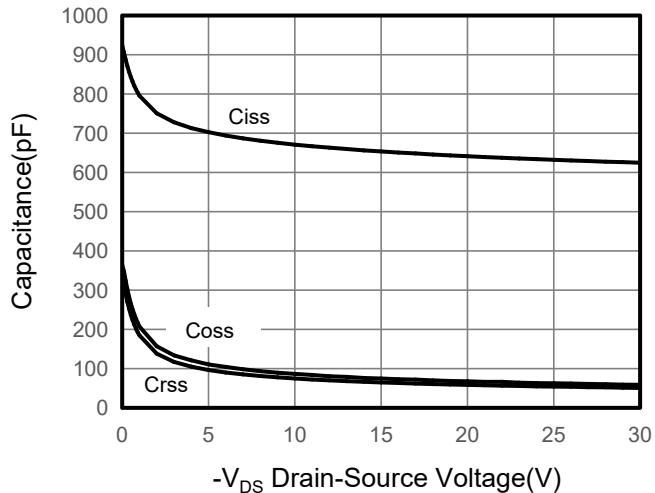
**Figure 3. Drain Source On Resistance**



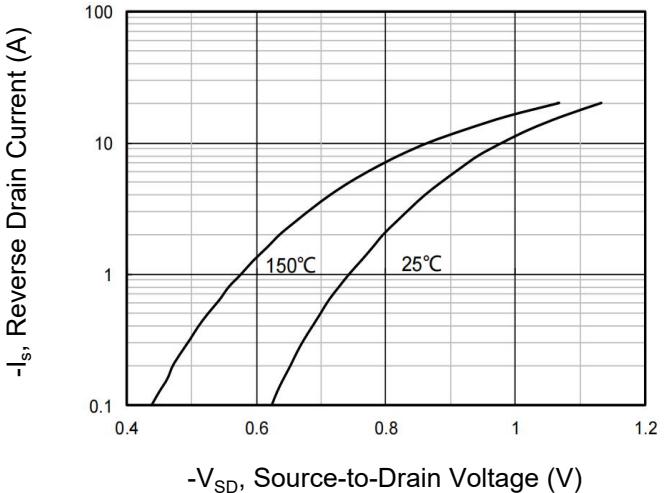
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

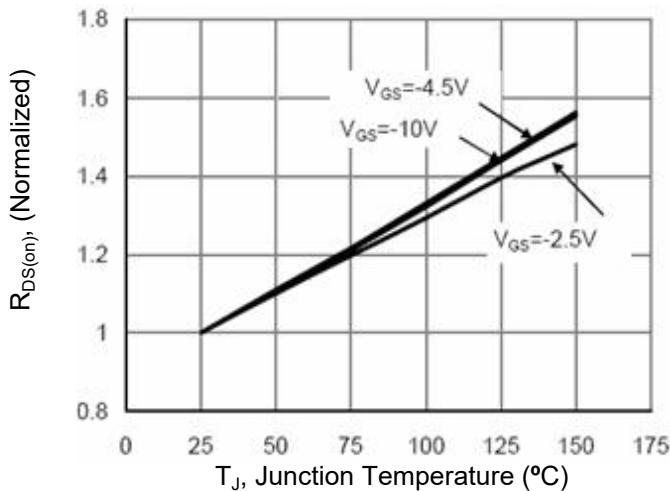


**Figure 6. Source-Drain Diode Forward**

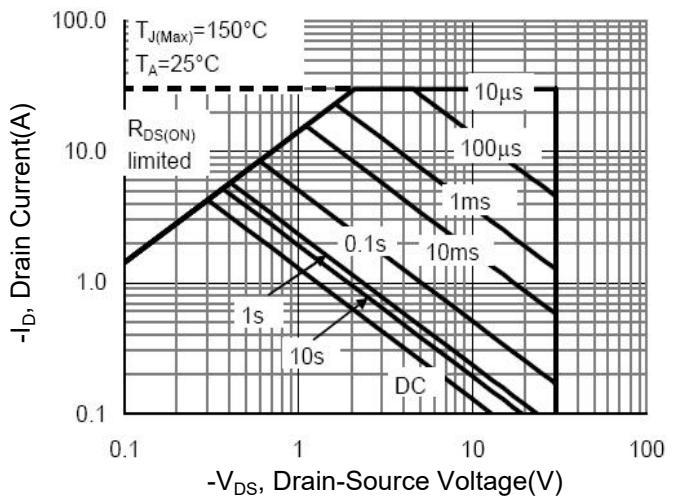


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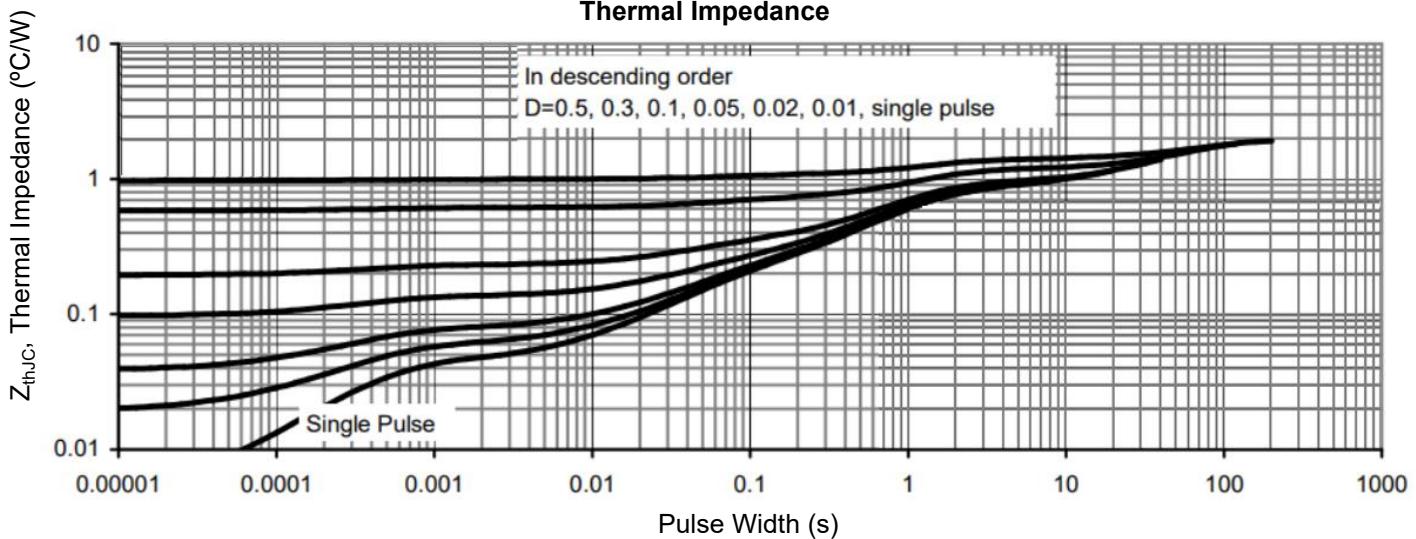
**Figure 7. Drain-Source On-Resistance**

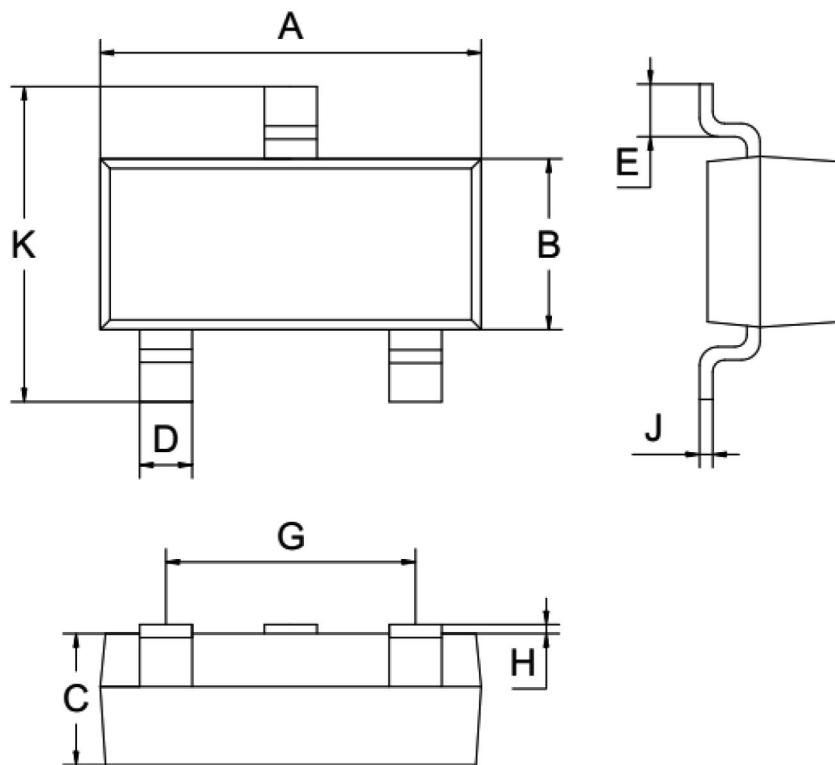


**Figure 10. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



**SOT-23-3L Package Information**

Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	2.80	2.90	3.00
B	1.50	1.60	1.70
C	1.00	1.10	1.20
D	0.30	0.40	0.50
E	0.25	0.40	0.55
G		1.90	
H	0.00	-	0.10
J	0.047	0.127	0.207
K	2.60	2.80	3.00

All Dimensions in mm