

# C6D08065E

650 V, 8 A Silicon Carbide Schottky Diode

## Features

- New 6<sup>th</sup> generation technology
- Low forward voltage drop ( $V_F$ )
- Zero reverse recovery current
- Zero forward recovery voltage
- Low leakage current ( $I_L$ )
- Temperature-independent switching behavior
- Positive temperature coefficient on  $V_F$



TO-252-2



Package Types: TO-252-2  
PN: C6D08065

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## Applications

- Switch mode power supplies (SMPS)
- Server/telecom power supplies
- Industrial power supplies
- Solar
- UPS

## Benefits

- Higher system level efficiency
- Increase system power density
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

## Maximum Ratings ( $T_c = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note
Repetitive Peak Reverse Voltage	$V_{RRM}$	650	V		
DC Blocking Voltage	$V_{DC}$	650			
Continuous Forward Current	$I_F$	29	A	$T_c = 25^\circ\text{C}$	Fig. 3
		15		$T_c = 125^\circ\text{C}$	
		8		$T_c = 155^\circ\text{C}$	
Repetitive Peak Forward Surge Current	$I_{FRM}$	34	A	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
		20		$T_c = 110^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Peak Forward Surge Current	$I_{FSM}$	63	A	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	Fig. 8
		57		$T_c = 110^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
	$I_{F, Max}$	781		$T_c = 25^\circ\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$	
		717		$T_c = 110^\circ\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$	
Power Dissipation	$P_{tot}$	85	W	$T_c = 25^\circ\text{C}$	Fig. 4
		37		$T_c = 110^\circ\text{C}$	
Operating Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +175	°C		



## Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Note
Forward Voltage	$V_F$	1.27	1.50	V	$I_F = 8 \text{ A}, T_J = 25^\circ\text{C}$	Fig. 1
		1.37	1.60		$I_F = 8 \text{ A}, T_J = 175^\circ\text{C}$	
Reverse Current	$I_R$	2	40	$\mu\text{A}$	$V_R = 650 \text{ V}, T_J = 25^\circ\text{C}$	Fig. 2
		15	160		$V_R = 650 \text{ V}, T_J = 175^\circ\text{C}$	
Total Capacitive Charge	$Q_C$	29		nC	$V_R = 400 \text{ V}, T_J = 25^\circ\text{C}$	Fig. 5
Total Capacitance	C	518		pF	$V_R = 0 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	Fig. 6
		57			$V_R = 200 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	
		45			$V_R = 400 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	$E_C$	4.4		$\mu\text{J}$	$V_R = 400 \text{ V}$	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

## Thermal Characteristics

Parameter	Symbol	Typ.	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.77	°C/W	Fig. 9

## Typical Performance

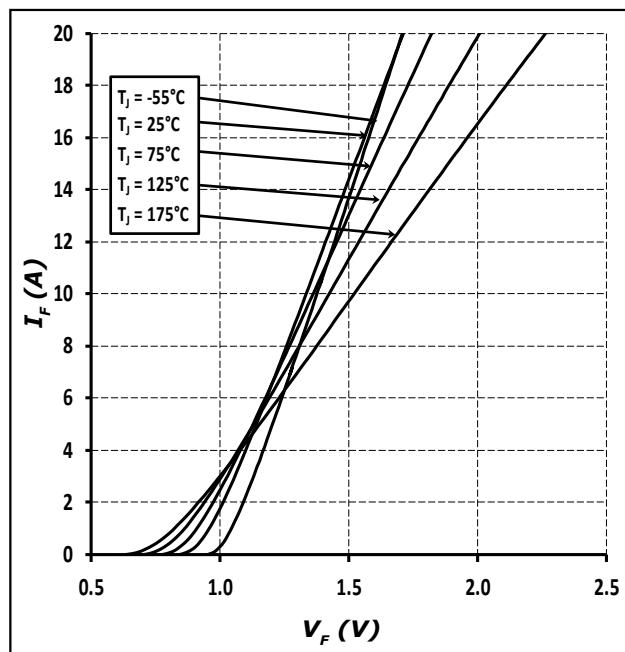


Figure 1. Forward Characteristics

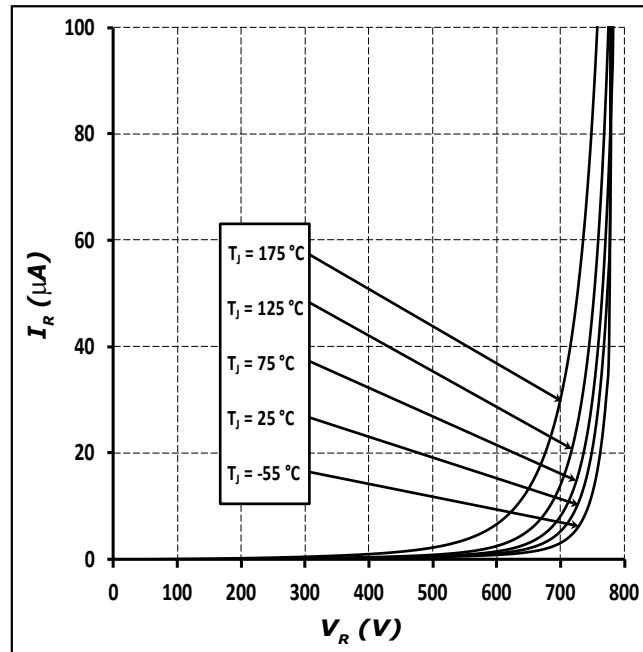


Figure 2. Reverse Characteristics



## Typical Performance

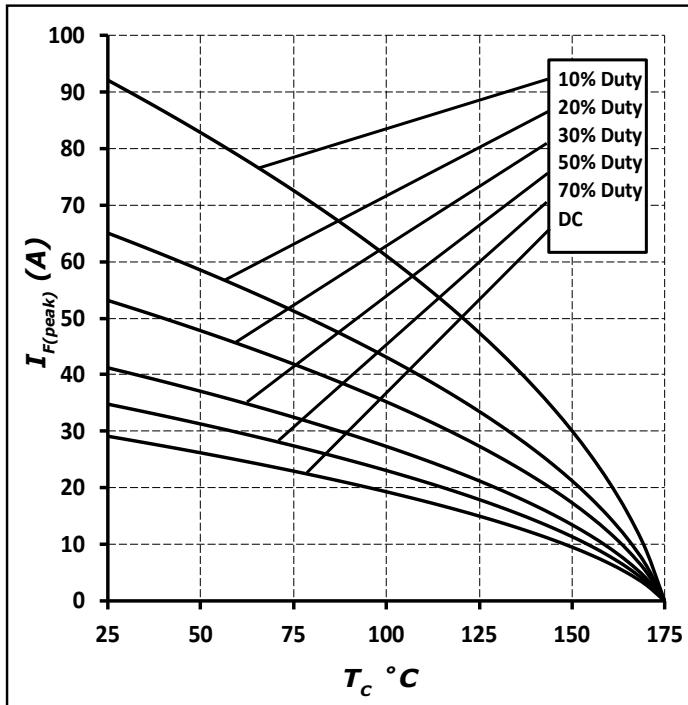


Figure 3. Current Derating

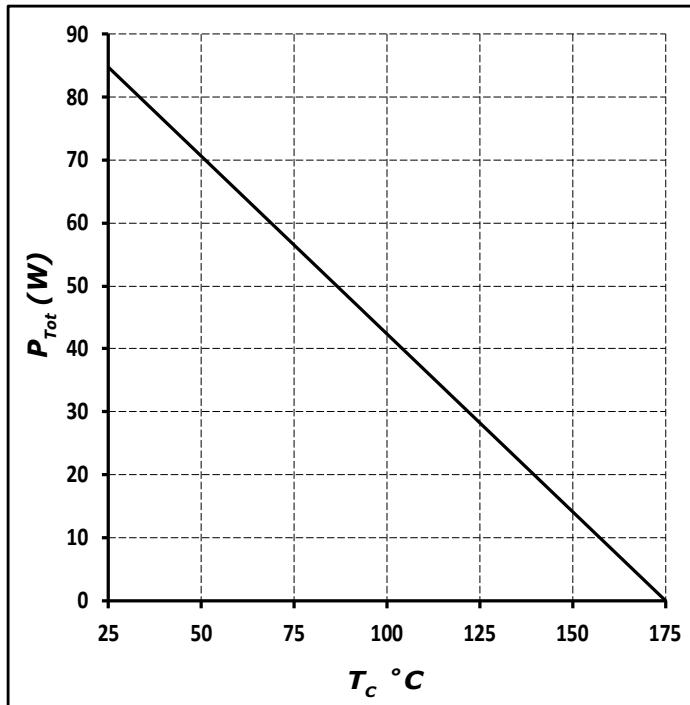


Figure 4. Power Derating

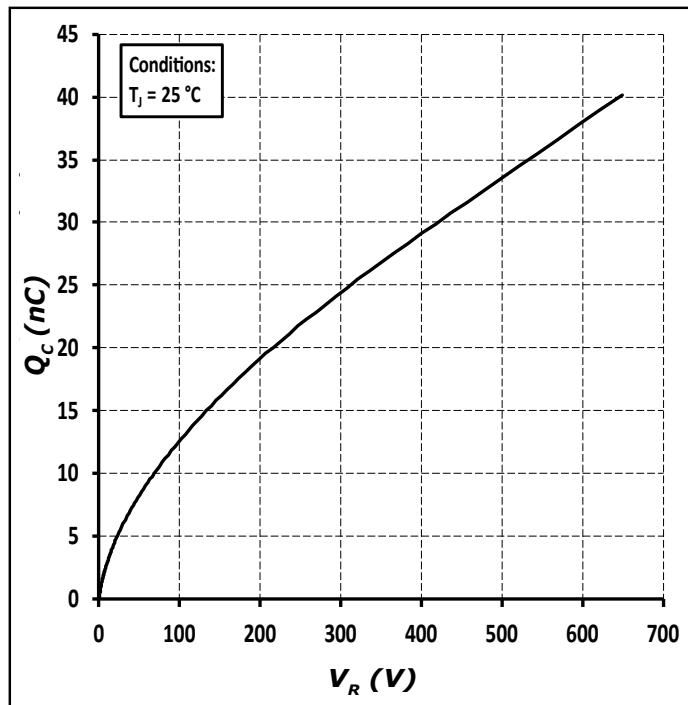


Figure 5. Total Capacitance Charge vs. Reverse Voltage

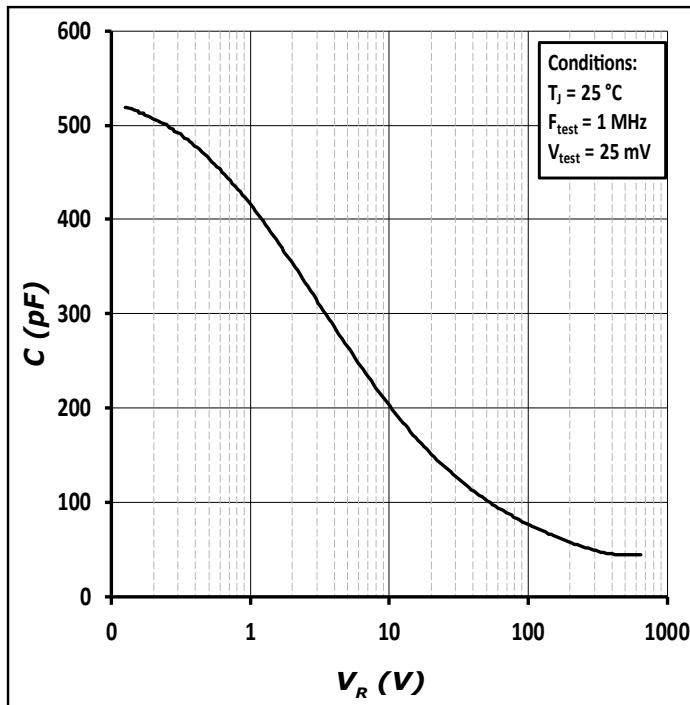


Figure 6. Capacitance vs. Reverse Voltage

## Typical Performance

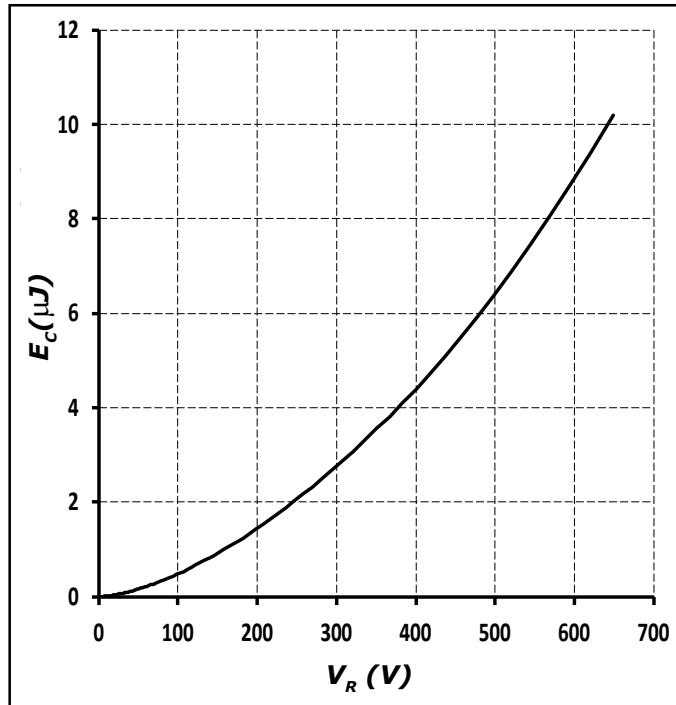


Figure 7. Capacitance Stored Energy

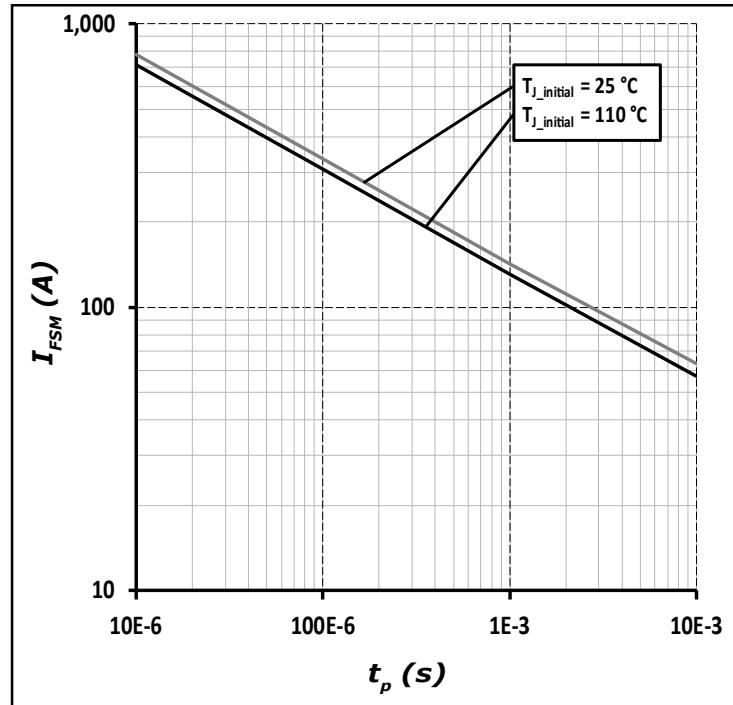


Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

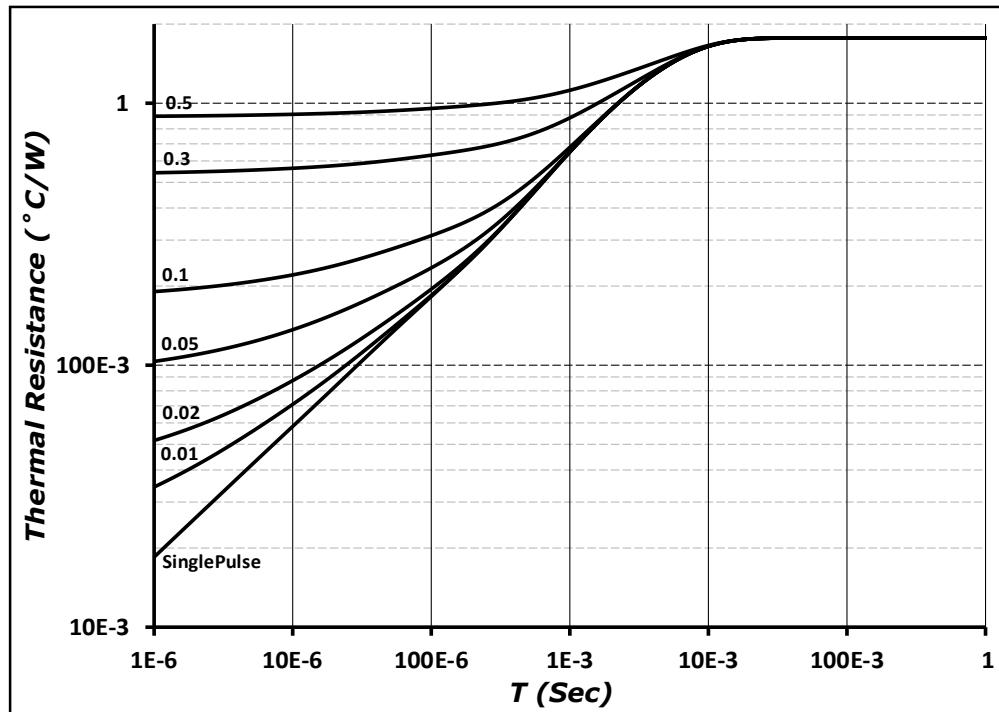
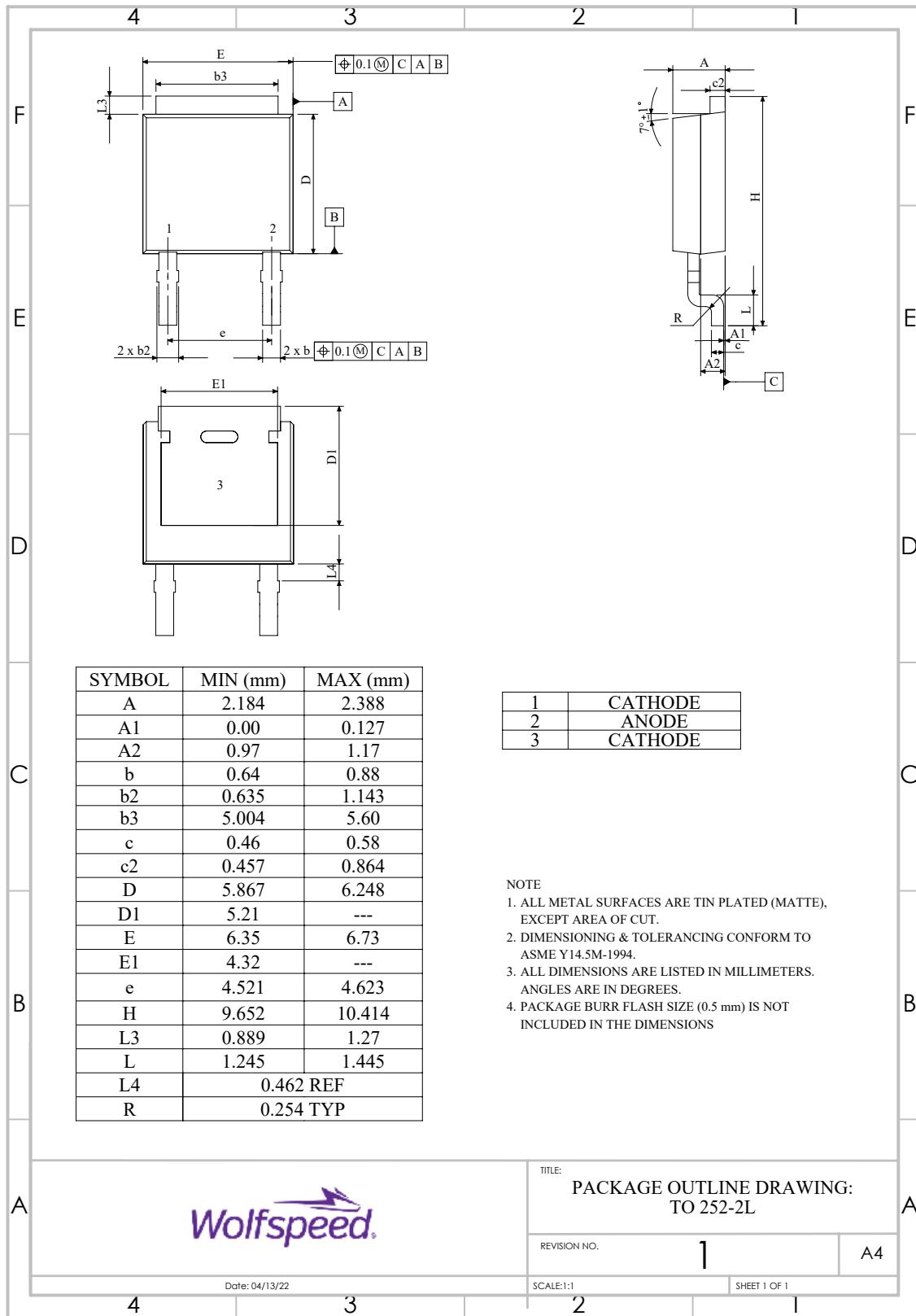


Figure 9. Transient Thermal Impedance

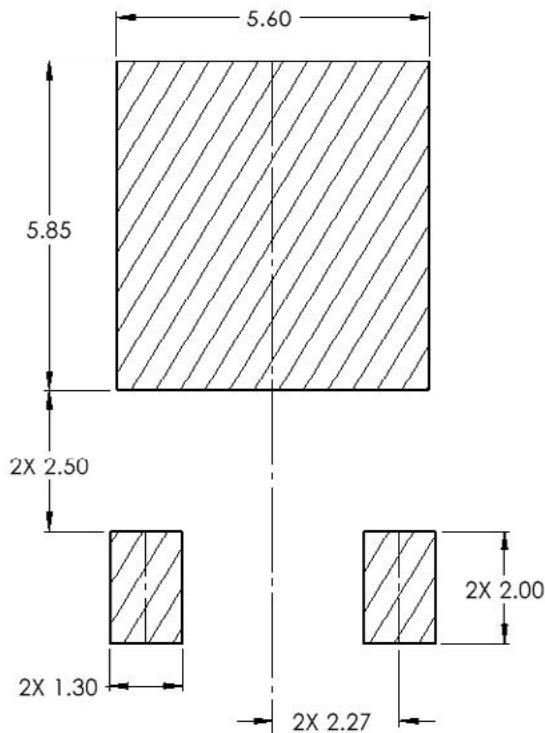
## Package Dimensions

Package: TO-252-2





## Recommended Solder Pad Layout



Part Number	Package	Marking
C6D08065E	TO-252-2	C6D08065



## Revision History

Current Revision	Date of Release	Description of Changes
2	September-2023	Updated Wolfspeed branding, package drawing, and solder pad layout
3	October-2023	Corrected solder pad layout, removed incorrect diode model



## Notes & Disclaimer

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