



DMP31D7L

Product Summary

BV _{DSS}	RDS(ON) Max	I _D TA = +25°C
-30V	0.9Ω @ VGS = -10V	-0.58A
-307	1.7Ω @ V _{GS} = -4.5V	-0.42A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

ESD protected Gate



SOT23

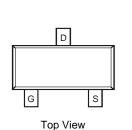
Top View

- Features
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q101, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

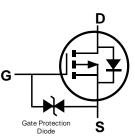
P-CHANNEL ENHANCEMENT MODE MOSFET

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



Internal Schematic



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Nulliber	Fackage	Qty.	Carrier	
DMP31D7L-7	SOT23	3000	Tape & Reel	
DMP31D7L-13	SOT23	10,000	Tape & Reel	

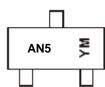
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



AN5 = Product Type Marking Code YM or \overline{Y} M = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2019		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G		I	J	K	L	М	Ν	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

oaturos



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	Vdss	-30	V		
Gate-Source Voltage	V _{GSS} ±20		V		
Continuous Drain Current (Note 6) V _{GS} = -4.5V	ID	-0.58 -0.46	A		
Maximum Continuous Body Diode Forward Current	ls	-0.52	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	b)		IDM	-2.5	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.43	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Rəja	290	°C/W
Total Power Dissipation (Note 6)		PD	0.46	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{ÐJA}	270	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

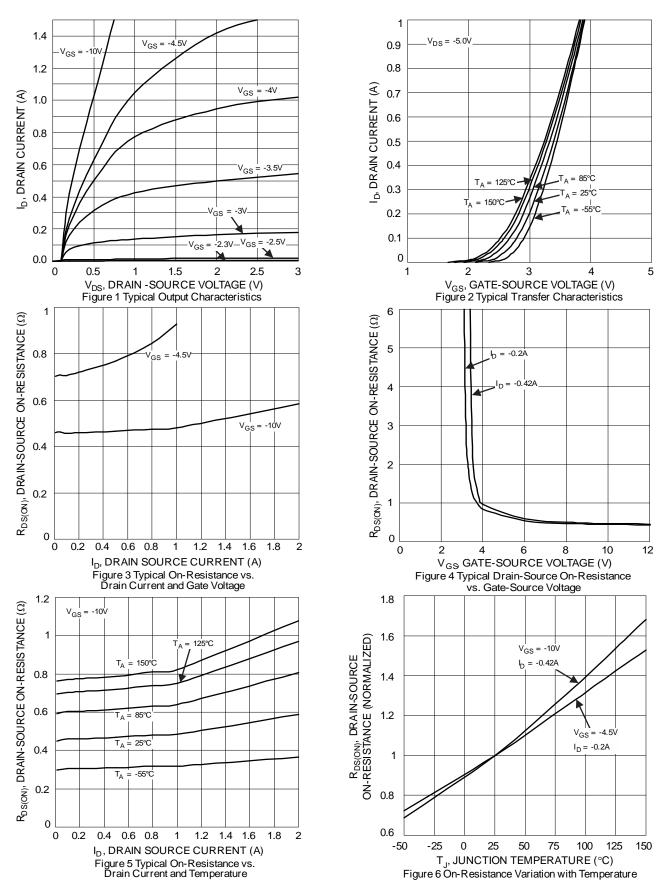
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BVDSS	-30	—	_	V	V _{GS} = 0V, I _D = -250µA
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	—	-1	μA	V _{DS} = -24V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						·
Gate Threshold Voltage	Vgs(th)	-1	—	-2.6	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
Static Drain-Source On-Resistance	C	-	0.4	0.9	Ω	VGS = -10V, ID = -0.42A
Static Drain-Source On-Resistance	R _{DS} (ON)	_	0.7	1.7	12	VGS = -4.5V, ID = -0.2A
Diode Forward Voltage	Vsd	_	-0.8	-1.2	V	V _{GS} = 0V, I _S = -0.23A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	19	_	pF	
Output Capacitance	Coss	-	16	_	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	3	_	pF	1 - 1.000112
Gate Resistance	Rg	_	729	_	Ω	$V_{DS} = V_{GS} = 0V$, f = 1.0MHz
Total Gate Charge	Qg	_	0.36	_	nC	
Gate-Source Charge	Qgs	_	0.1	_	nC	Vgs = -4.5V, Vps = -10V, Ip = -250mA
Gate-Drain Charge	Q _{gd}	_	0.1	_	nC	10 = -23011A
Turn-On Delay Time	td(on)	_	30	_	ns	
Turn-On Rise Time	tR	_	74	_	ns	$V_{DD} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	tD(OFF)	_	28	_	ns	$R_L = 47\Omega, R_G = 10\Omega,$ ID = -200mA
Turn-Off Fall Time	t _F	_	31	_	ns	

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

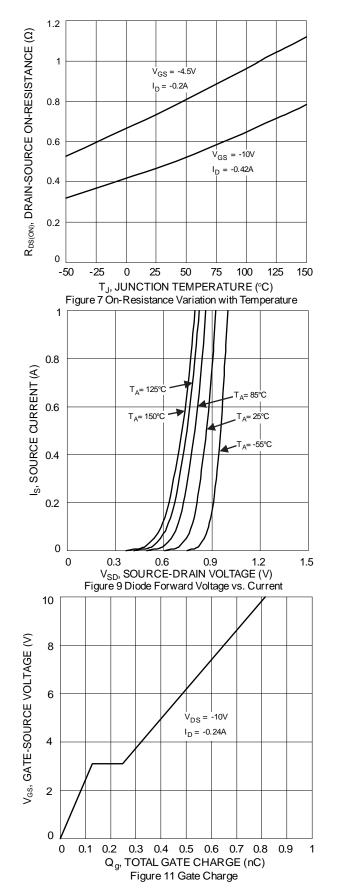
8. Guaranteed by design. Not subject to product testing.

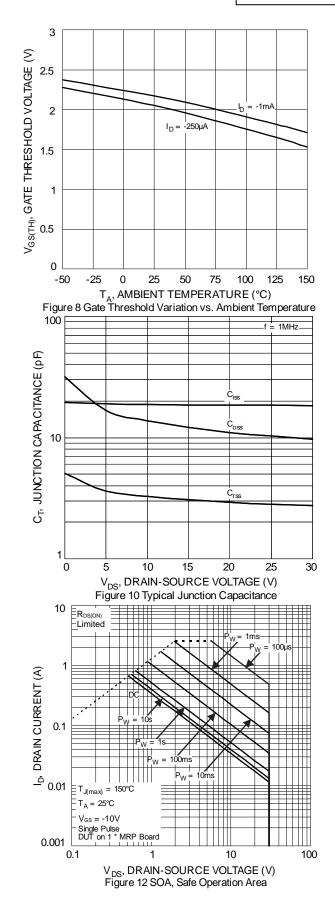




DMP31D7L Document number: DS41890 Rev. 3 - 2







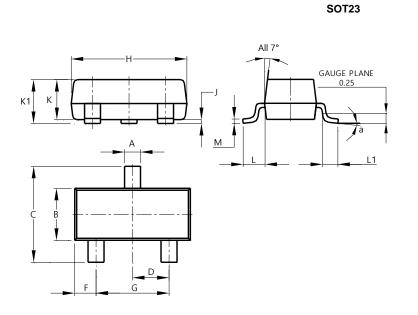






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

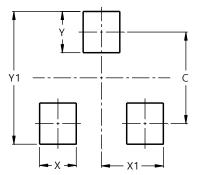


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	Dimens	ions in	mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

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DMP31D7L



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